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4	BRS	L62	1	(computer adj program) and (microcircuit same simula$4)
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6	BRS	L64	0	(computer adj program) and (microcircuit same design$3) and (goal adj state)
7	BRS	L65	7	(goal adj state).clm.
8	BRS	L66	6	(goal adj state).clm. and (simulat$4 or model$4)

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The algorithmic analysis of hybrid systems - group of 16 »
R Alur, C Courcoubetis, N Halbwachs, TA Henzinger, ... - Theoretical Computer Science, 1995 - www-verimag.imag.fr
Abstract Wepresent a general framework fortheformal specication and algorithmic analysis ofhybrid systems. Ahybrid system consists of a discrete program ...
Cited by 627 - View as HTML - Web Search - BL Direct

HYTECH: a model checker for hybrid systems - group of 9 »
TA Henzinger, PH Ho, H Wong-Toi - International Journal on Software Tools for Technology ..., 1997 - Springer
Abstract. A hybrid system is a dynamical system whose behavior exhibits both discrete and continuous change. A hybrid automaton is a mathematical model ...
Cited by 388 - Web Search - Library Search - BL Direct

Hybrid Automata: An Algorithmic Approach to the Specification and Verification of Hybrid Systems - group of 6 »
R Alur, C Courcoubetis, TA Henzinger, PH Ho - LECTURE NOTES IN COMPUTER SCIENCE, 1993 - portal.acm.org
... **Pei-Hsin Ho**, Publisher, Springer-Verlag London, UK. ... Pei Ho. **Pei-Hsin Ho**. Gerard J. Holzmann. Yerang Hur. Alon Itai. Franjo Ivancic. Lalita Jategaonkar Jagadeesan ...
Cited by 368 - Web Search - Library Search - BL Direct

Automatic Symbolic Verification of Embedded Systems - group of 10 »
R Alur, TA Henzinger, PH Ho - IEEE Real-Time Systems Symposium, 1993 - www-cad.eecs.berkeley.edu
Hybrid systems are digital real-time systems that are embedded in analog environments. Due to the rapid development of digital-processor technology, ...
Cited by 310 - View as HTML - Web Search - BL Direct

[PS] A user guide to HyTech - group of 7 »
TA Henzinger, PH Ho, H Wong-Toi - TACAS, 1995 - www-cad.eecs.berkeley.edu
Abstract HyTech is a tool for the automated analysis of embedded systems. This document, designed for the rst-time user of HyTech , guides the reader ...
Cited by 182 - View as HTML - Web Search - BL Direct

HYTECH: The Next Generation - group of 9 »
TA Henzingert, PH Ho, H Wong-Toit - doi.ieeecomputersociety.org
HY TECHI, a symbolic model checker for hybrid sys- tems. Given a parametric description of an embedded system as a collection of communicating ...
Cited by 149 - Web Search

Algorithmic analysis of nonlinear hybrid systems - group of 10 »
TA Henzinger, PH Ho, H Wong-Toi - IEEE Transactions on Automatic Control, 1998 - ieeexplore.ieee.org
Abstract— Hybrid systems are digital real-time systems that are embedded in analog environments. Model-checking tools are available for the automatic ...
Cited by 122 - Web Search - Library Search - BL Direct

[PS] HYTECH: The Cornell HYbrid TECHnology Tool - group of 10 »
TA Henzinger, PH Ho - LECTURE NOTES IN COMPUTER SCIENCE, 1995 - eecs.berkeley.edu
Thomas A. Henzinger and **Pei-Hsin Ho** ... Computer Science Department, Cornell University, Ithaca, NY 14853 (tah j ho)@cs.cornell.edu

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Automated Analysis of an Audio Control Protocol - group of 8 »
PH Ho, H Wong-Toi - CAV, 1995 - eecs.berkeley.edu
Motivated by the desire to verify real-life reactive systems, Bosscher et al.
BPV94] met with engineers at Philips, Netherlands, and developed a formal ...
Cited by 86 - View as HTML - Web Search - BL Direct

Smart Simulation Using Collaborative Formal and Simulation Engines - group of 15 »
PH Ho, T Shiple, K Harer, J Kukula, R Damiano, V ... - IEEE ACM INT CONF COMPUT AIDED DES DIG TECH PAP. pp. 120-126 ..., 2000 - doi.ieeecomputersociety.org
We present Ketchum , a tool that was developed to improve the productivity of simulation-based functional verification by providing two capabilities: (1) ...
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Modeling and Analysis of Real-Time Ada Tasking Programs - group of 3 »
JC Corbett - IEEE Real-Time Systems Symposium, 1994 - ieeexplore.ieee.org
Page 1 Modeling and Analysis of Real-Time Ada Tasking Programs* 1052-8725/94 $04.00
3 1994 IEEE 132 James C. Corbett Department of Information and Computer ...
Cited by 20 - Web Search

Optimal conditional reachability for multi-priced timed automata - group of 2 »
KG Larsen, JI Rasmussen - To appear, 2004 - Springer
... If the intersection is non-empty, the minimum primary cost of any **goal state** satisfying
the constraint on the secondary cost is computed and compared to Cost ...
Cited by 4 - Web Search

UPPAAL-Now, Next, and Future - group of 15 »
T Amnell, G Behrmann, J Bengtsson, PR D'Argenio, A ... - LECTURE NOTES IN COMPUTER SCIENCE, 2001 - Springer
... This can be expressed with a timed reachability question, and if the **goal state**
is reachable, the trace gives also a feasible schedule. ...
Cited by 29 - Web Search - BL Direct

Adaptive Sample Bias for Rapidly-exploring Random Trees with Applications to Test Generation - group of 3 »
J Kim, JM Esposito - Proc. American Control Conference - ieeexplore.ieee.org
... Other sampling strategies which bias the samples in a region closer to the
goal state have been tried in [17] and [5] with some success. ...
Cited by 1 - Web Search

An RRT-based algorithm for testing and validating multi-robot controllers - group of 2 »
J Kim, JM Esposito, V Kumar - Robotics: Science and Systems Conference, MIT, Cambridge, MA ..., 2005 - seas.upenn.edu
... systems. Biasing the sampling toward regions close to the **goal state** has
been tried in [14], [15] and [3] with some success. However ...
Cited by 1 - View as HTML - Web Search

Analogy and Mysticism and the Structure of Culture - group of 2 »
S Klein - Current Anthropology, 1983 - JSTOR
... If we wish to make a plan that specifies more than one **goal state** in the ... as an example
of the Vajradhatu group, the Gobu Shinkan (Wu-pu **Hsin**-kuan) brought back ...
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3	BRS	L3	0	(microcircuit$2 same verif$4) and (random near simula$6)
4	BRS	L4	1	(microcircuit$2 same verif$4) and (goal same state)
5	BRS	L5	405	717/106.ccls.
6	BRS	L6	0	(circuits near (goal ad states))
7	BRS	L7	0	(circuits near (goal adj states))
8	BRS	L8	276	(goal adj states)
9	BRS	L9	0	(goal adj states) and microcircuits and simula$6
10	BRS	L10	0	(goal adj states) and microcircuits and simula$4
11	BRS	L11	0	(goal adj states) and microcircuits and simulation
12	BRS	L12	0	(goal adj states) and microcircuits
13	BRS	L13	22	(simulation same microcircuits)
14	BRS	L14	50	(simula$4 same microcircuits)
15	BRS	L15	13	(simula$4 same microcircuits) and (sequen$2)
16	BRS	L16	1	(simula$4 same microcircuits) and ((sequen$2) near simulat$$)
17	BRS	L17	1	(simula$4 same microcircuits) and ((sequen$2) near simulat$4)
18	BRS	L18	6	(simula$4 same microcircuits) and (simula$4 same veri$5)
19	BRS	L20	3	(simula$4 same microcircuits) and (simula$4 same veri$5) and (sequen$4) and vectors and random$3
20	BRS	L21	848	703/13.ccls.
21	BRS	L23	0	703/13.ccls. and (simula$4 near random) and (simulat$3 near sequence)
22	BRS	L22	8	703/13.ccls. and (simula$4 near random)
23	BRS	L24	3	703/13.ccls. and (simula$4 near random) and vector
24	BRS	L25	0	703/13.ccls. and (simula$4 near random) and (vector near sequence)

25	BRS	L19	5	(simula$4 same microcircuits) and (simula$4 same veri$5) and (sequen$4)

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	Type	L #	Hits	Search Text
26	BRS	L26	1	(simula$4 same microcircuits) and (simula$4 same veri$5) and (sequen$4) and monitor$3
27	BRS	L27	75302	(data adj processing adj system)
28	BRS	L28	1	(data adj processing adj system) and (microcircuit same veri$3)
29	BRS	L29	0	(data adj processing adj system) and (microcircuit same veri$3) and simulation
30	BRS	L30	0	(data adj processing adj system) and (microcircuit same veri$3) and simula$4
31	BRS	L31	0	(data adj processing adj system) and (microcircuit same veri$3) and (simula$4 or model$3)
32	BRS	L32	1	(data adj processing adj system) and (microcircuit same simula$4)
33	BRS	L33	0	(data adj processing adj system) and (microcircuit same simula$4) and (computer same medium same read)
34	BRS	L34	0	(data adj processing adj system) and (microcircuit same simula$4) and (computer same medium)
35	BRS	L35	0	(data adj processing adj system) and (microcircuit same simula$4) and (computer same program same produc)
36	BRS	L36	0	(data adj processing adj system) and (microcircuit same simula$4) and (computer same program same product)
37	BRS	L37	0	(microcircuit same simula$4) and (computer same program same product)
38	BRS	L38	11	(microcircuit same simula$4) and (computer same program)
39	BRS	L39	3	(microcircuit same simula$4) and (computer same program) and (veri$4 and sequence)
40	BRS	L40	0	(microcircuit same simula$4) and (computer same program same product) and (veri$4 and sequence)
41	BRS	L41	0	(microcircuit near simula$4) and (computer same program) and (veri$4 and sequence)
42	BRS	L42	0	(microcircuit near simula$4) and (computer near program) and (veri$4 and sequence)
43	BRS	L43	69587	(computer adj program adj product)
44	BRS	L44	491	(computer adj program adj product) and (user near medium)
45	BRS	L45	0	(computer adj program adj product) and (user near medium) and (computer same microcircuit)
46	BRS	L46	0	(computer adj program adj product) and (user near medium) and (computer near microcircuit)

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29	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/03/02 14:17		
30	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/03/02 14:18		
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33	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/03/02 14:21		
34	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/03/02 14:22		
35	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/03/02 14:22		
36	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/03/02 14:22		
37	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/03/02 14:22		
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39	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/03/02 14:24		
40	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/03/02 14:23		
41	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/03/02 14:24		
42	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/03/02 14:25		
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47	BRS	L47	0	(computer adj program adj product) and (user near medium) and (computer near/3 microcircuit)
48	BRS	L48	0	(computer adj program adj product) and (user near medium) and (microcircuit)]
49	BRS	L49	0	(computer adj program adj product) and (user near medium) and (microcircuit)
50	BRS	L50	13	(computer adj program adj product) and (microcircuit)
51	BRS	L51	9	(computer adj program adj product) and (microcircuit) and simulat$4 and (computer same code)
52	BRS	L52	0	(computer adj program adj product) and (microcircuit) and simulat$4 and (computer same code) and (random near simulat$4)
53	BRS	L53	0	(computer adj program adj product) and (microcircuit) and simulat$4 and (computer same code) and (random adj simulat$4)
54	BRS	L54	5	(computer adj program adj product) and (microcircuit) and simulat$4 and (computer same code) and (monitor or veri$3)
55	BRS	L55	1483	714/4.ccls.
56	BRS	L56	1	714/4.ccls. and microcircuits

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4	BRS	L62	1	(computer adj program) and (microcircuit same simula$4)
5	BRS	L63	4	(computer adj program) and (microcircuit same design$3)
6	BRS	L64	0	(computer adj program) and (microcircuit same design$3) and (goal adj state)
7	BRS	L65	7	(goal adj state).clm.
8	BRS	L66	6	(goal adj state).clm. and (simulat$4 or model$4)
9	BRS	L67	2331	microcircuits
10	BRS	L68	2247	microcircuits not simulation
11	BRS	L69	1	microcircuits same master same slave
12	BRS	L70	152	717/104.ccls.